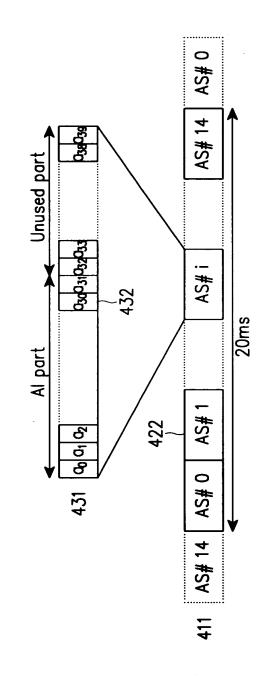


FIG. 4



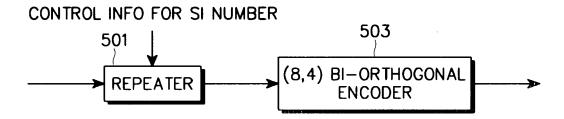


FIG. 5

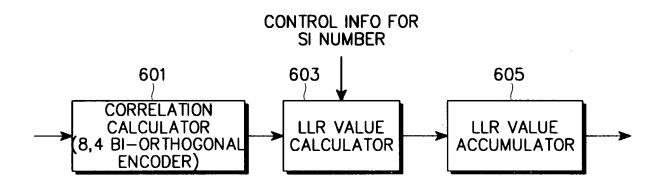


FIG. 6

1.

FIG. 7

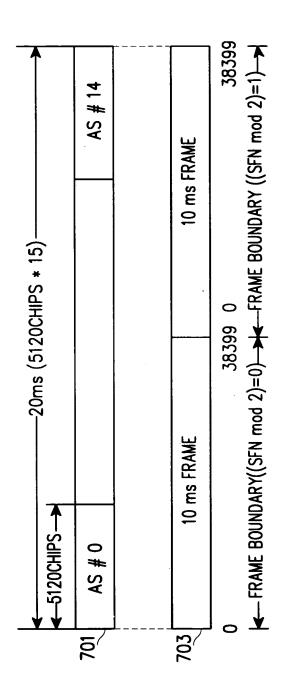


FIG. 8B

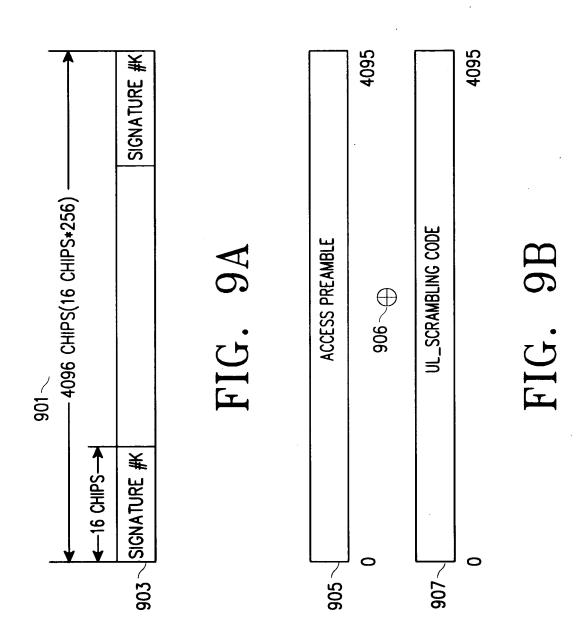


FIG. 10A

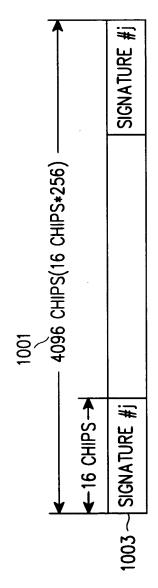
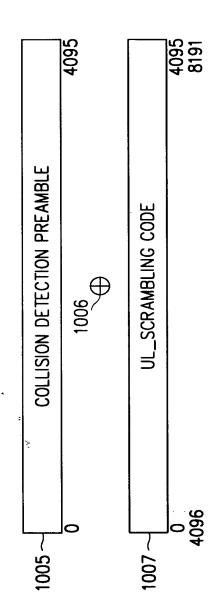
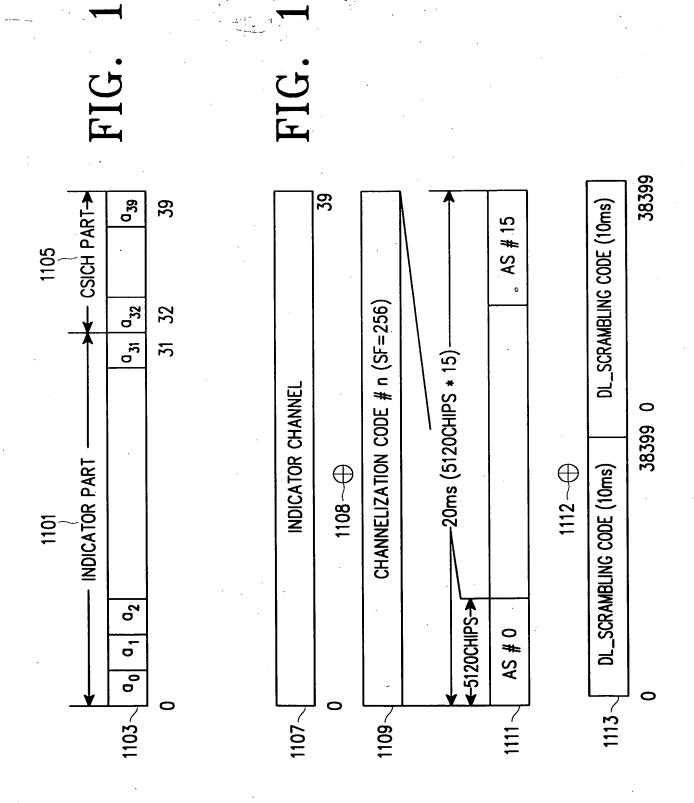


FIG. 10B





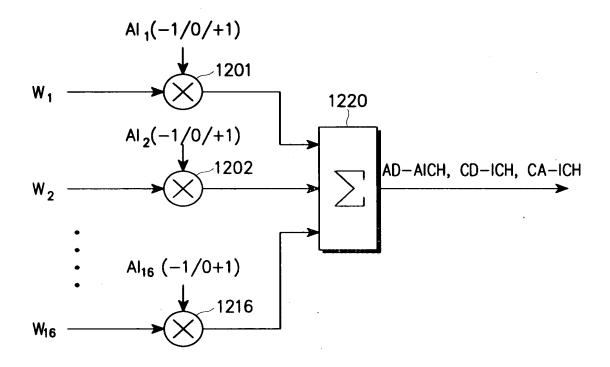
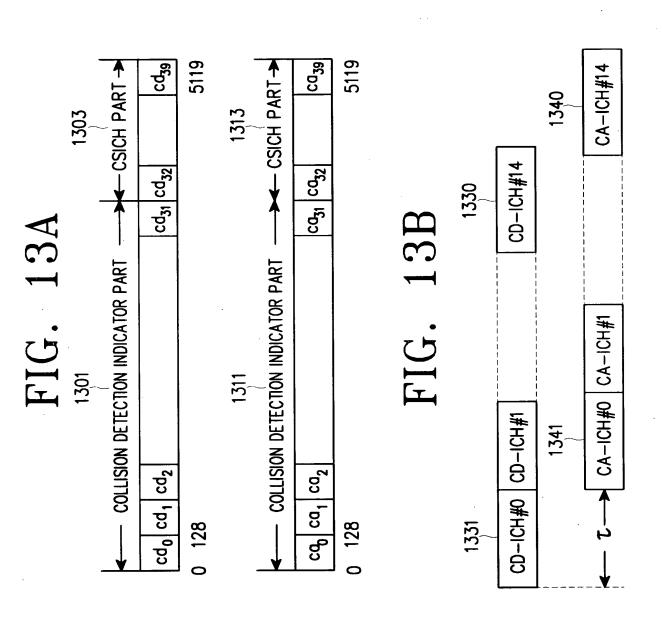


FIG. 12



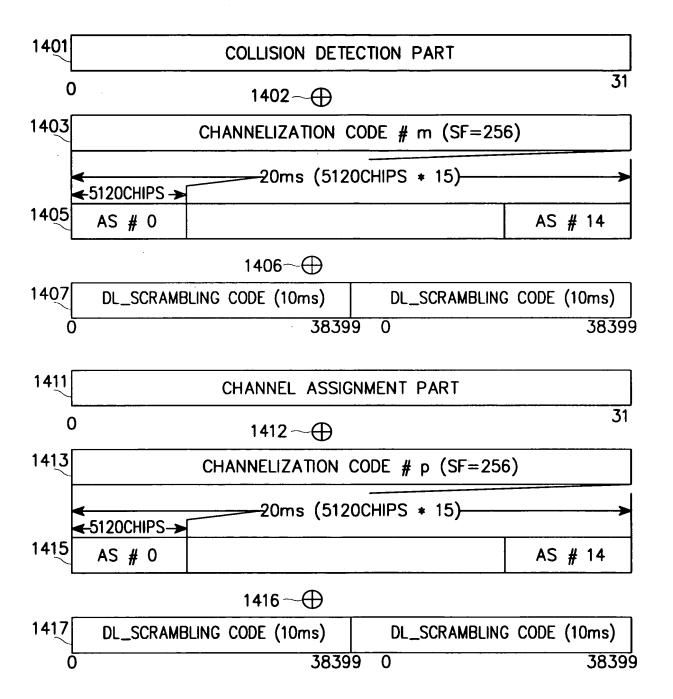


FIG. 14

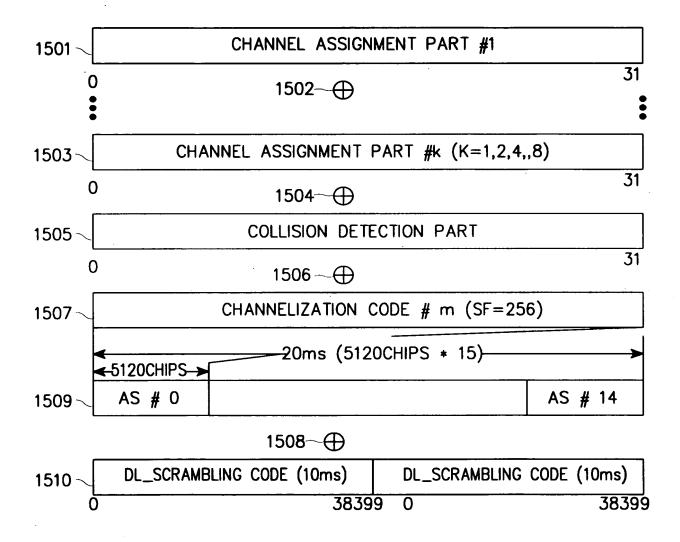
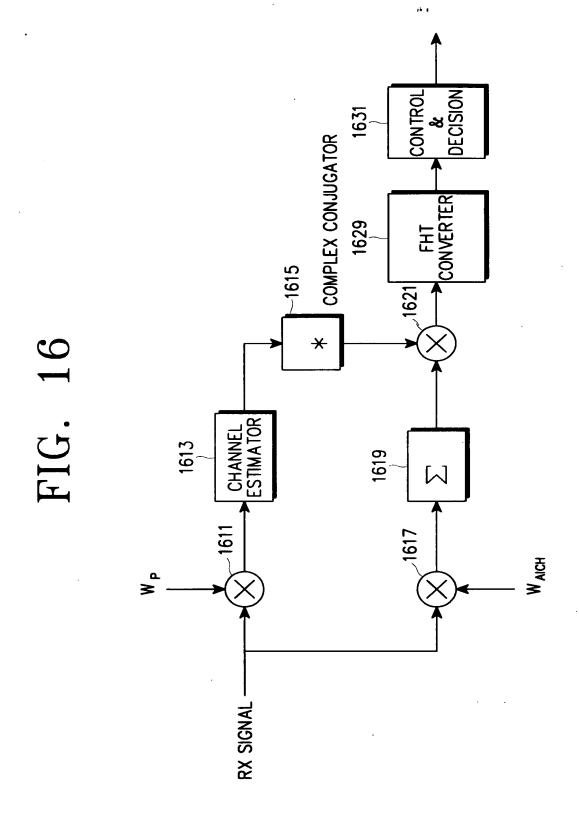


FIG. 15



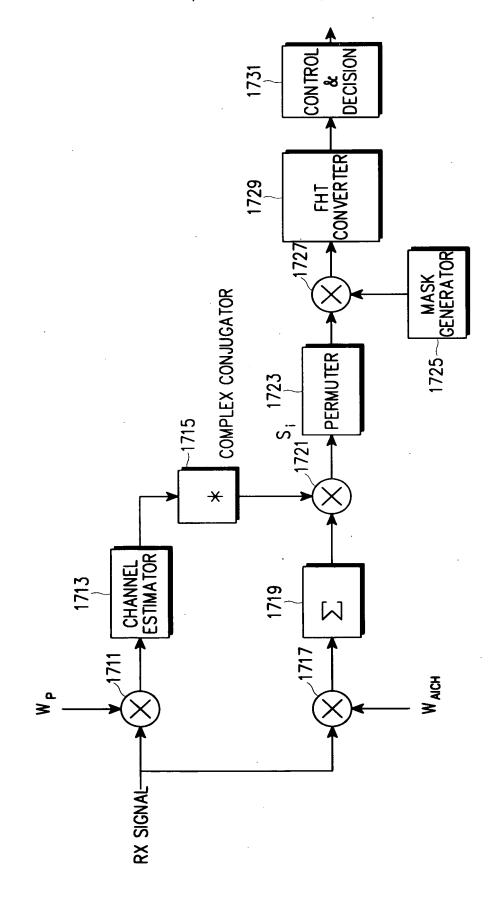


FIG. 17

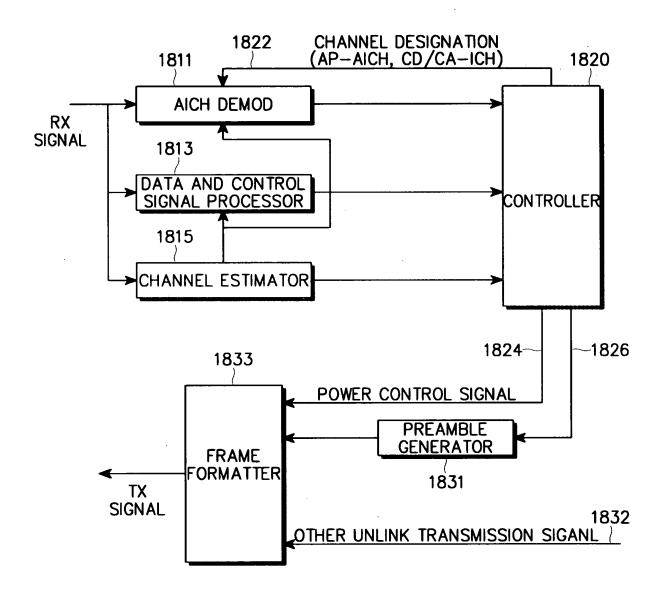


FIG. 18

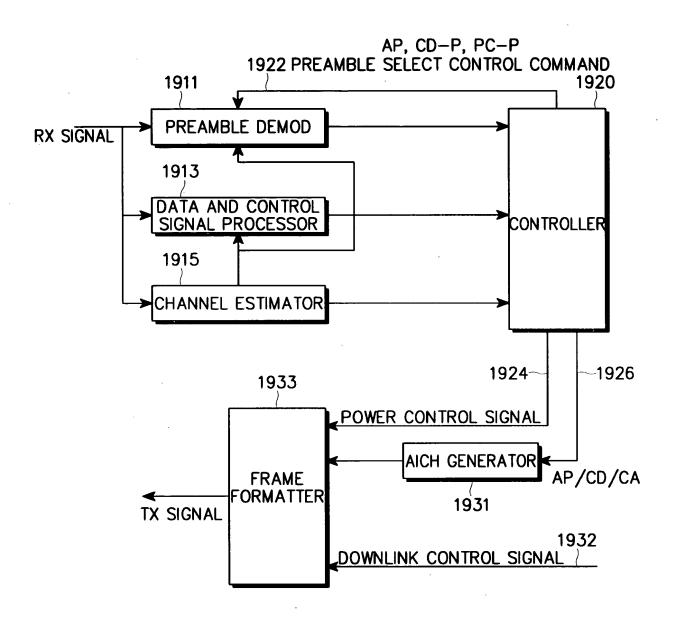


FIG. 19

	<b>2001</b>	2003	<b>2005</b>
	N <sub>PILOT</sub> (8 OR 7 BITS)	N <sub>FBI</sub> (0 OR 1 BITS)	N <sub>TPC</sub> (2 BITS)
0			9

FIG. 20

FIG. 21

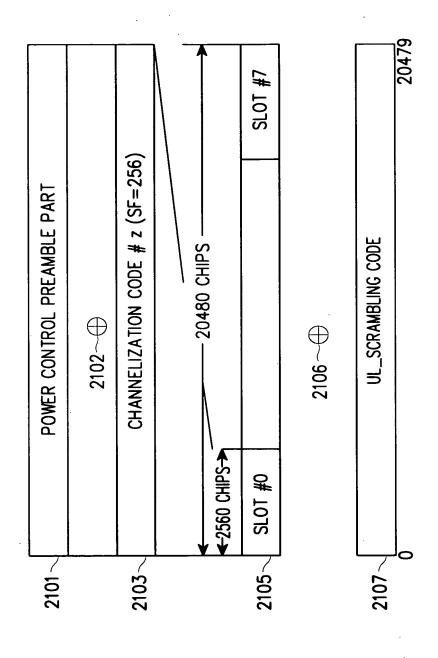


FIG. 22A

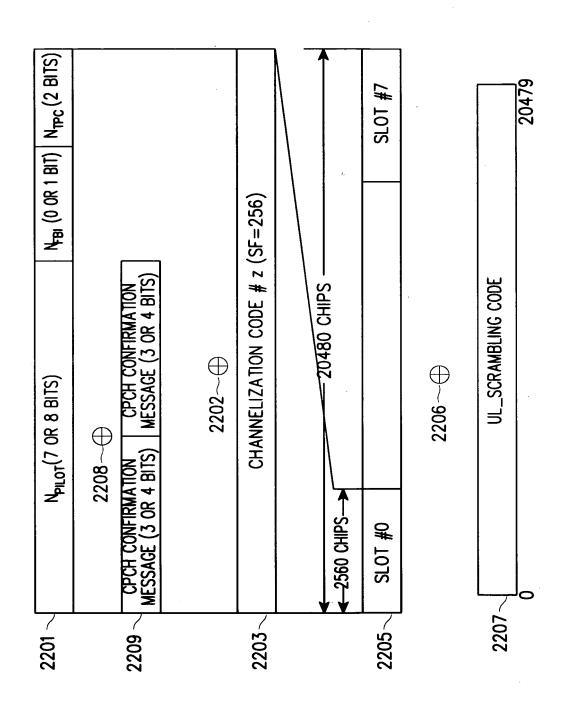


FIG. 22B

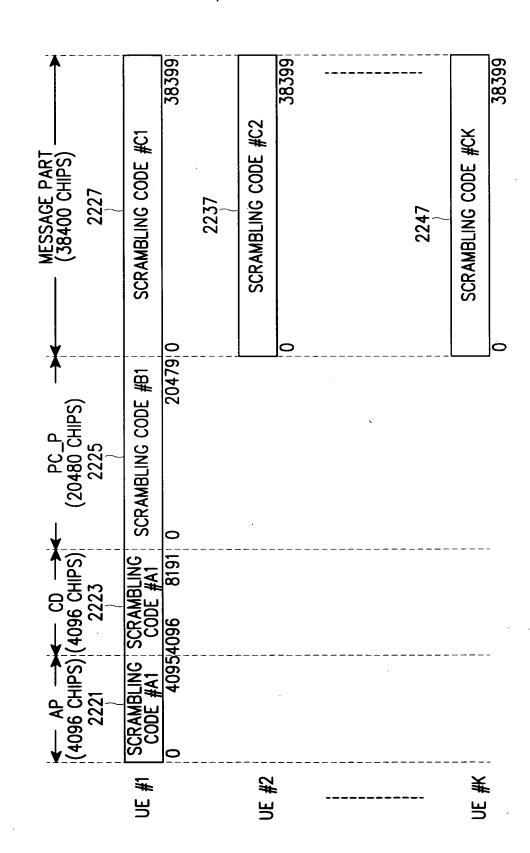


FIG. 23

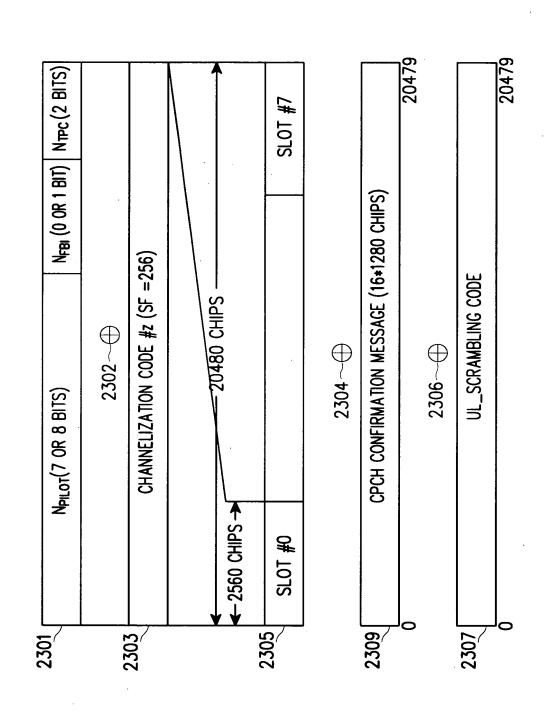
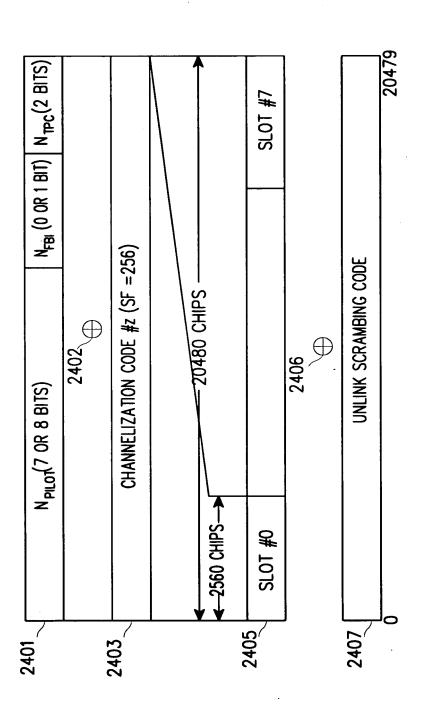


FIG. 24A



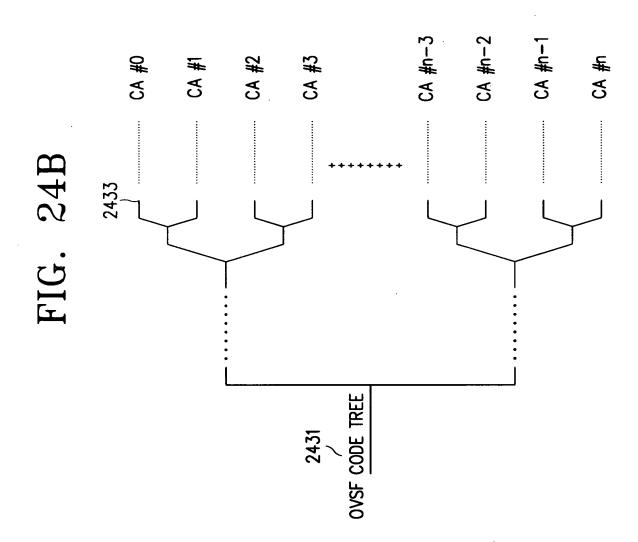


FIG. 25A

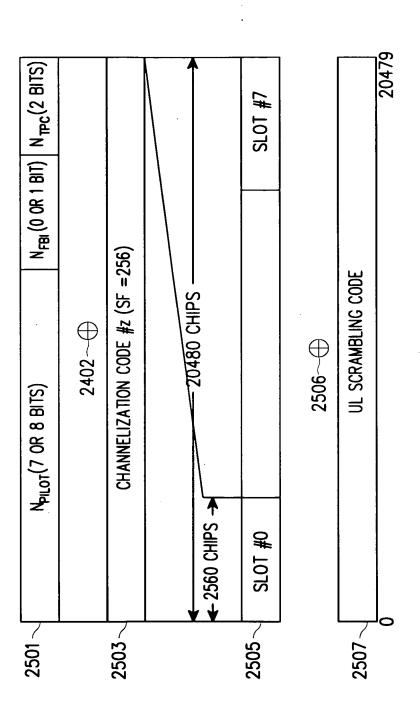
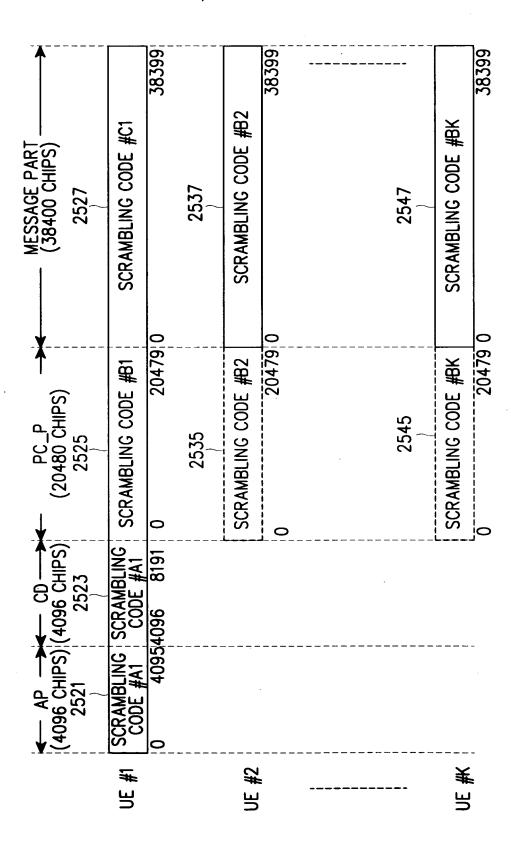
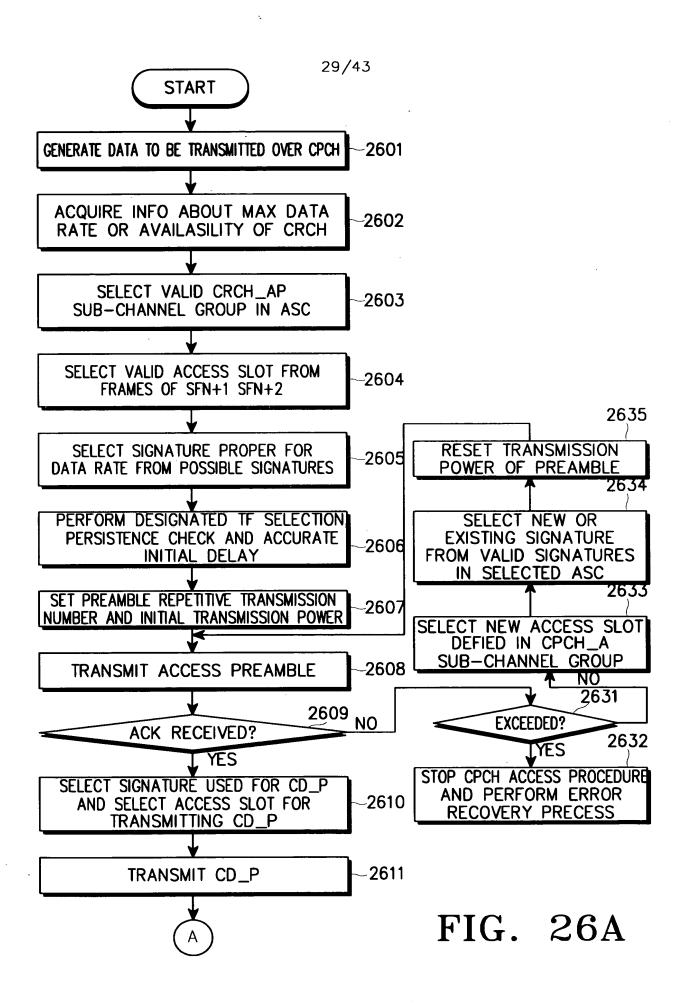


FIG. 25B





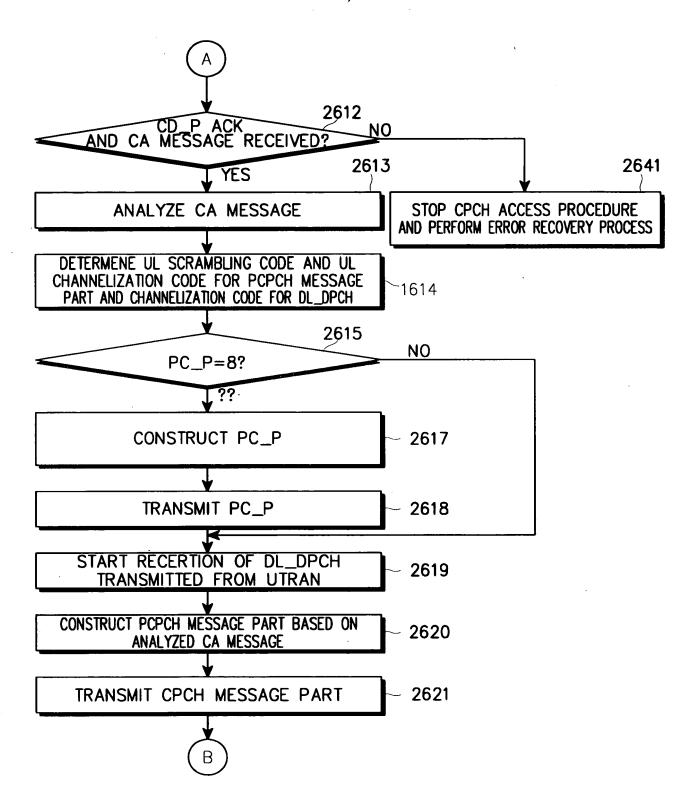


FIG. 26B

## FLOWCHART OF CPCH USING CHANNEL ASSIGNMENT (UE SIDE)

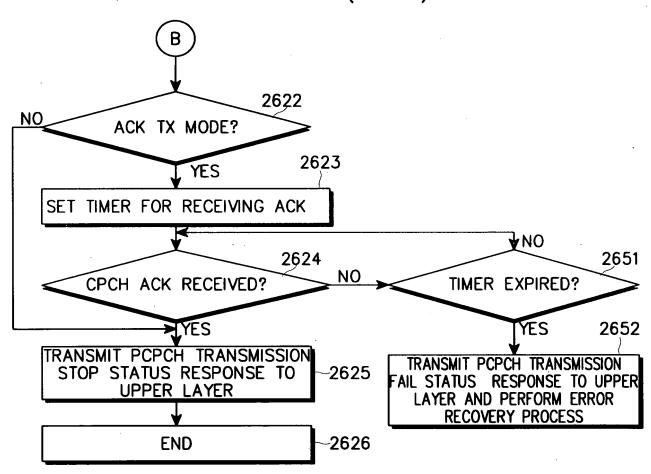


FIG. 26C

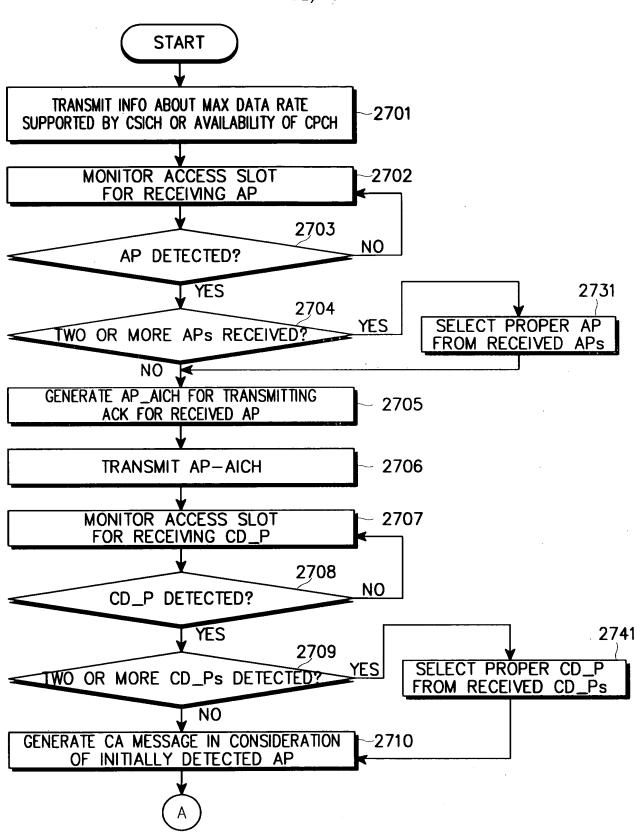


FIG. 27A

FIG. 27B

## FLOWCHART OF CPCH USING CHANNEL ASSIGNMENT (UTRAN SIDE)

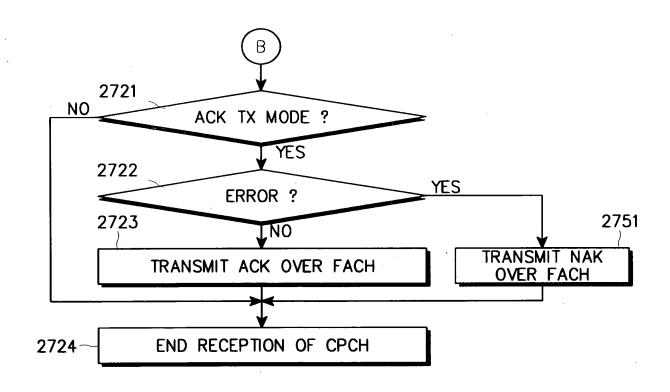


FIG. 27C

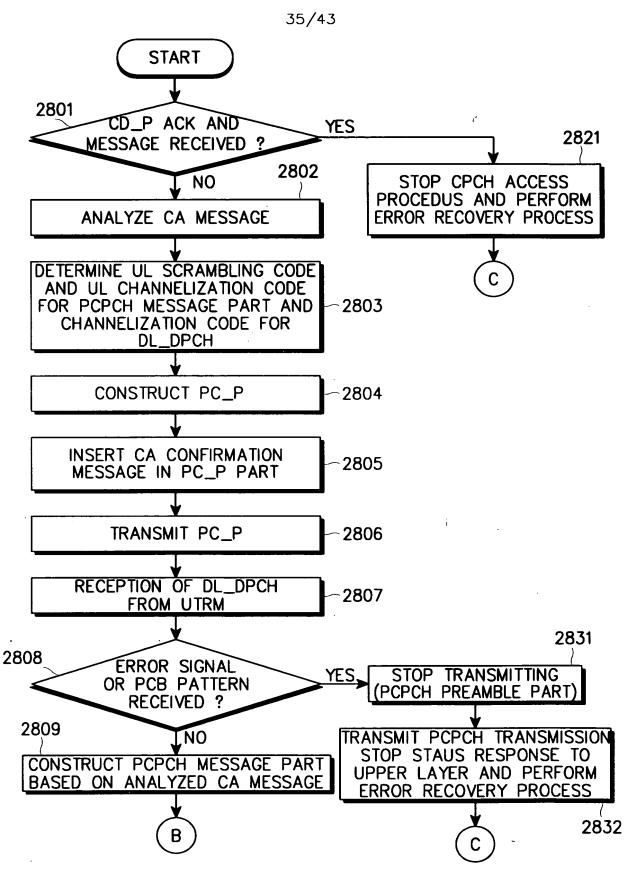


FIG. 28A

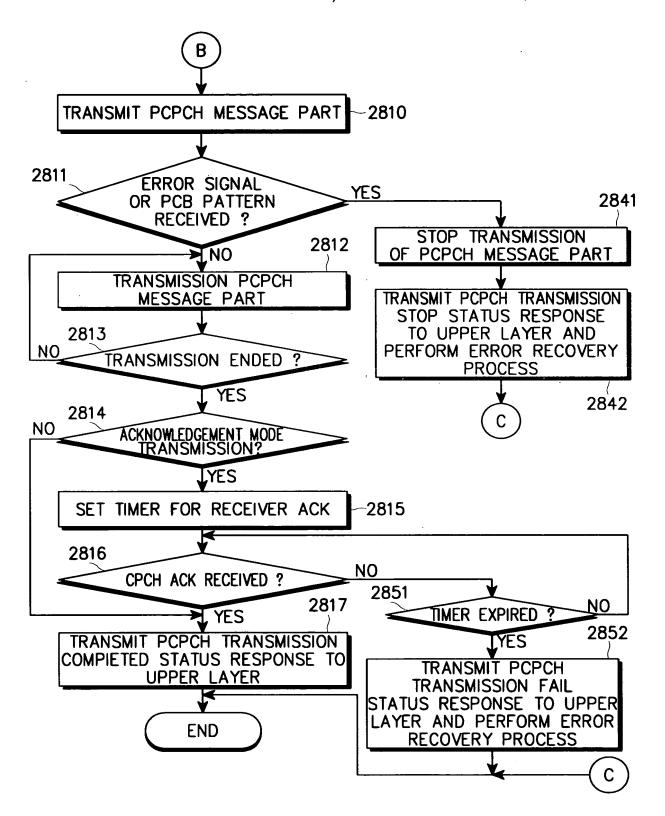
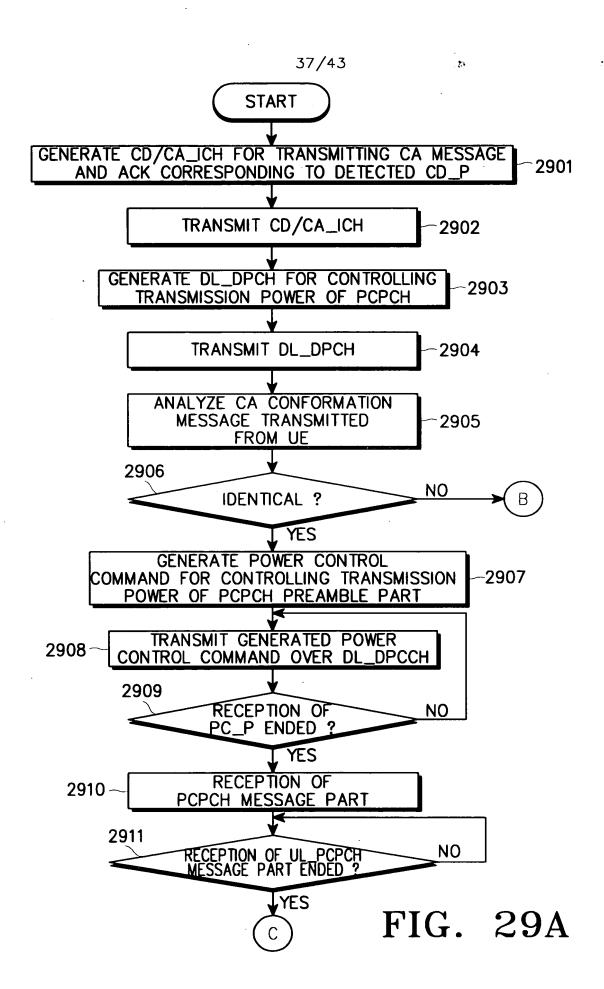


FIG. 28B



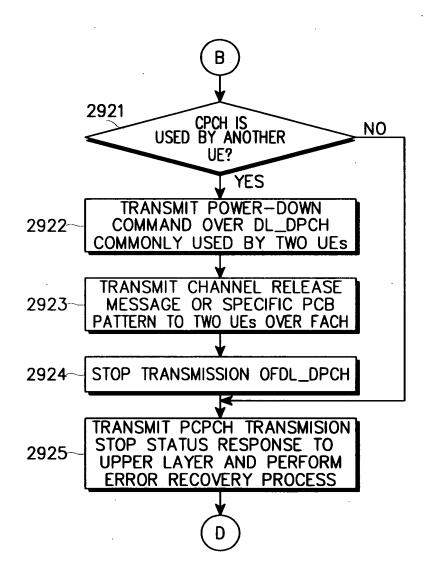


FIG. 29B

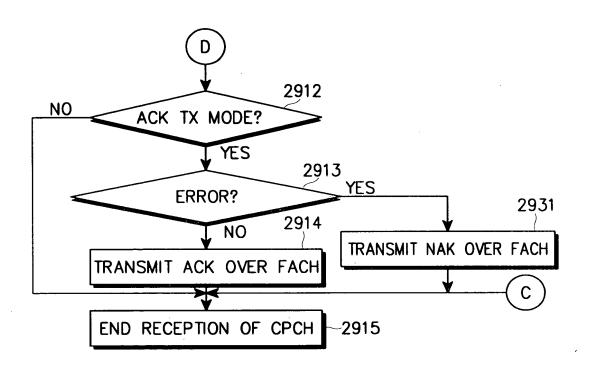


FIG. 29C

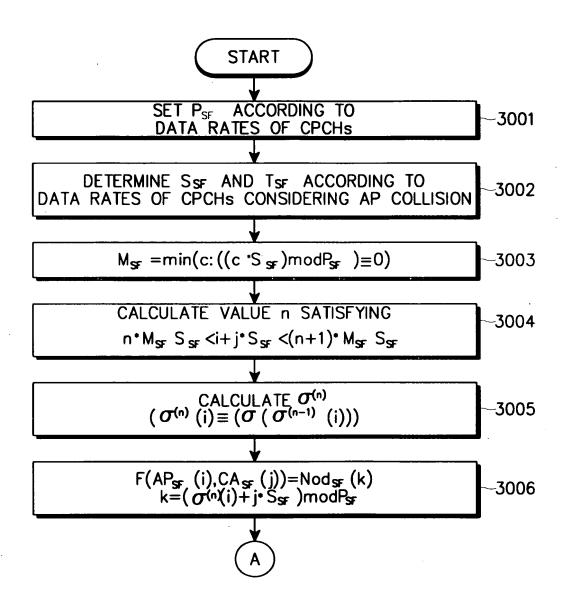


FIG. 30A

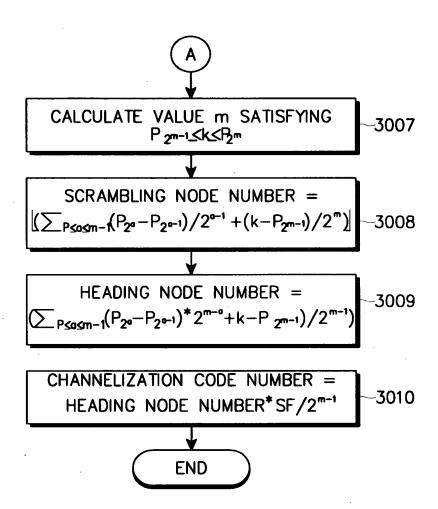


FIG. 30B

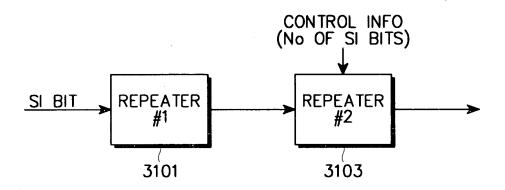


FIG. 31

